

Jonathan Michael Georgino

365 Talbot Avenue, Apt T2, Pacifica, CA 94044

Phone: (814) 594-0637

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**Education:**

**Politecnico di Milano** 2010/2011

Completed First Year of Master of Science in Computing Systems Engineering

Admitted to 7<sup>th</sup> Cycle of Alta Scuola Politecnico

Awarded Full Academic Scholarship

**Pennsylvania State University – The Behrend College** Class of 2010

Dual Concurrent B.S. Degrees in Electrical Engineering & Computer Engineering

3.43 Cumulative GPA

**BridgeTEFL** May 2009

60-Hour Certificate in Teaching English as a Foreign Language

Certificate Number TO0011132

**Elk County Catholic High School** Class of 2006

Graduated Magna Cum Laude

**Technical Skills:**

**Design Tools:** Altium Designer, MATLAB, LTSpice, ADS, IAR Embedded Workbench

**Programming:** C/C#(.NET)/C++, Assembly, VHDL, Visual Basic, HTML, PHP, SQL, version control with GIT.

**Hands-On:** Embedded System Design, Firmware Development, PCB Layout & 3D Modeling, Component Selection & BOM Management, PCB assembly and bring-up, hands-on debugging and rework, Test bench design & automation.

**Work Experience:**

- Wonder Workshop – Hardware Engineer (March 2014 – Present)  
*San Mateo, California*  
As the only electrical engineer on the hardware/firmware team, I played a critical role in the design, validation, and production phases of Dash & Dot robots. This involved rapid prototyping and testing, as well as frequent trips to our CM in China. I also developed and maintained all of the ECAD (PCB layouts, schematics, footprints, etc), as well as kept up the lab. Other responsibilities include design explorations and prototyping of future products as well as performing failure analysis on returned units. Also worked with a number of external testing facilities to ensure all compliance requirements (FCC, Bluetooth SIG, Toy Safety, etc) were met before mass production.
- Saleae – Hardware Engineer (October 2012 – March 2014)  
*San Francisco, California*  
Responsibilities included development of new devices from design simulation and schematic capture through component selection, PCB layout, and board assembly, bring-up, and testing. Develop and maintain firmware for common Microchip, TI, Atmel, and Cypress microcontrollers, as well as coding for internal testing tools and our commercial software application. Manage entire supply chain at the electronic components level for assembly of our existing products and established and maintain relationships with local CMs and suppliers as we brought our production back to the USA from Asian facilities. Provide technical customer support via phone and email.

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- GE Advanced Sensors – Design Engineer (July 2011 – October 2012)  
*Saint Marys, Pennsylvania*  
Supported multidisciplinary team with new product development from inception to volume production. Responsibilities included creation of software test plan and testing automation for embedded systems sensor devices, development of software tools for testing and production, design of experiments for performance testing, and data analysis. Designed & automated test bench for debugging and validation of system performance. Regularly interacted with external customers, suppliers, and contractors in support of new product development and production launch. Significant experience with CAN / SAE J-1939 communication protocol and debugging tools. Redesigned software tools for simplifying manufacturing processes. Implemented SPC system for product line. Gained experience in a tier-1 automotive supplier manufacturing facility.
- Associazione Culturale Linguistica Educational (Summer 2008/2009, Academic Year 2010/2011)  
*Italy*  
Inspired and motivated Italian children to learn and practice the English language through games, songs, and drama directly incorporated with traditional language teaching methods. Demonstrated concepts of using drama in education at many workshops for ESL teacher training, and participated in many conferences on Content Language Integrated Learning (CLIL).
- Morgan Advanced Materials & Technology, Inc. (Summer 2005/2006/2007)  
*Saint Marys, Pennsylvania*  
Worked in a laboratory environment conducting physical properties testing on both finish grade products and raw materials. Certified product to customer specifications according to ISO standards.

**Seminars, Presentations, Awards, and Honors:**

- SAE World Congress – April 25 – 27, 2012 – Detroit, MI  
Attended 2-day seminar on Advanced Diesel After-Treatment System Technologies
- IEEE Region 2 Student Paper Competition - 1<sup>st</sup> Place – Wireless Gear Shifter  
Presented April 17<sup>th</sup>, 2010 at IEEE Region 2 Student Activities Conference at Temple University in Philadelphia, Pennsylvania.s
- Effective Diagnosis and Monitoring of Aortic Insufficiency Using Biomedical Signals  
Presented November 6<sup>th</sup>, 2009 at 5<sup>th</sup> Annual Science Consortium at TREC, Erie, PA
- IEEE Student Paper Competition, Erie Chapter– 3<sup>rd</sup> Place - Color Detection with FPGA
- Tau Beta Nu Engineering Honor Society Member - Provisional chapter of Tau Beta Pi
- Completed Behrend Honors and Scholars Program

**Interests:**

Interests include playing guitar, piano, and music theory. I dabble in the performance arts of unicycling and juggling, occasionally at the same time. I am fascinated with foreign cultures and languages, and thus love traveling. I'm also an avid host for Couchsurfers.

**Languages:**

English (Mother Tongue), Italian (Advanced), Portuguese (Intermediate), Spanish (Beginner)